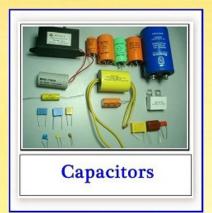
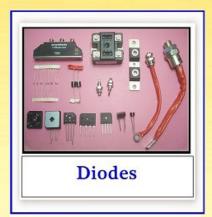
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2MBI150VB-120-50

IGBT Modules

IGBT MODULE (V series) 1200V / 150A / 2 in one package

■ Features

High speed switching Voltage drive Low Inductance module structure

■ Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



■ Maximum Ratings and Characteristics

◆ Absolute Maximum Ratings (at T_c=25°C unless otherwise specified)

Items Collector-Emitter voltage Gate-Emitter voltage		Symbols	Conditions		Maximum ratings	Units	
		Vces			1200	V	
		V _{GES}	1 KON 2010 MARK NOT 1127 MARK		±20	V	
Collector current		Ic	Continuous	Tc=100°C	150		
		C pulse	1ms	•	300		
		-lc		17	150		
		- C pulse	1ms	10	300		
Collector power dissipation		Pc	1 device	55	1070	W	
Junction temper	rature	Tj			175		
Operating junction temperature (under switching conditions)		Tjop			150	°C	
Case temperature		Tc			125	C	
Storage temperature		Tstg			-40 ~ 125		
Isolation voltage between terminal and copper base (*1)		Viso	AC:1min.		2500	VAC	
Screw torque	Mounting (*2)	-		EE:	3.5	N m	
	Terminals (*3)	-			3.5	iv m	

Note *1: All terminals should be connected together during the test.

Note *2: Recommendable Value : 2.5-3.5 Nm (M5 or M6) Note *3: Recommendable Value : 2.5-3.5 Nm (M5)

● Electrical characteristics (at T_i= 25°C unless otherwise specified)

Items	Comphete:	0l			Characteristics		
nems	Symbols Conditions			min.	typ.	max.	Units
Zero gate voltage collector current	Ices	ces V _{GE} = 0V, V _{CE} = 1200V		1.50	-	2.0	mA
Gate-Emitter leakage current	IGES	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	-	400	nA
Gate-Emitter threshold voltage	er threshold voltage $V_{GE(B)}$ $V_{CE} = 20V$, $I_{C} = 150$ mA		62	6.0	6.5	7.0	V
	V _{CE (sat)} (terminal)	V _{GE} = 15V I _C = 150A	T _i =25°C	-	2.00	2.45	V
			T _i =125°C	-	2.35	-	
Callaster Emitter esturation valtage			T _j =150°C	3	2.40	2000000000000	
Collector-Emitter saturation voltage	V _{GE (sat)} (chip)	V _{GE} = 15V I _C = 150A	T _i =25°C	-	1.85	2.30	
			T _i =125°C	-	2.20	-	
			T _j =150°C		2.25		
Internal gate resistance	R _{G (int)}			1=1	5.0	1=1	Ω
Input capacitance	Cies	Vce = 10V, Vge = 0V, f =	_	12.0	121	nF	
	ton	Vcc = 600V Ls = 30nH Ic = 150A		1.	600	150	nsec
Turn-on time	t			-	200		
	t _{r (i)}	V _{GE} = ±15V	-	50	-		
Turn-off time	t _{off}	$R_G = 4.2\Omega$		-	800	121	
ium-on ume	tr	T _i = 150°C	-	80	-		
	VF	V - 0V	T _i =25°C	9-9	1.85	2.30	V
Enward on valtage	(terminal)	V _{GE} = 0V I _F = 150A	T _i =125°C	-	2.00	121	
		IF - 130A	T _i =150°C		1.95		
Forward on voltage	V _F (chip)	V _{GE} = 0V	T _i =25°C	-	1.70	2.15	
		V _{GE} = UV I _F = 150A	T _i =125°C		1.85	(*)	
		IF - 150A	T _j =150°C		1.80		0
Reverse recovery time	t _r	I _F = 150A		-	150	-	nsec

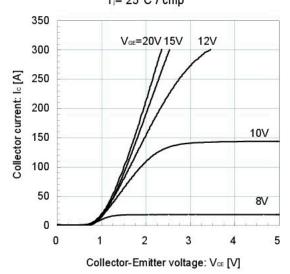
Thermal resistance characteristics

Items	Symbols	Conditions	Ch	Characteristics			
items	Symbols		min.	typ.	max.	Units	
Thermal resistance (Identica)	Rth(j-c)	IGBT			0.140		
Thermal resistance (1device)		FWD	-	-	0.200	°C/W	
Contact thermal resistance (1device) (*4)	Rth(c-f)	with Thermal Compound	-	0.025	(-1)		

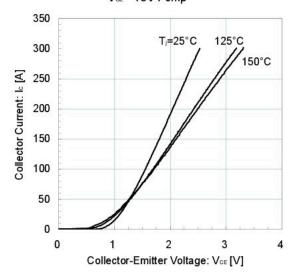
Note *4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Characteristics (Representative)

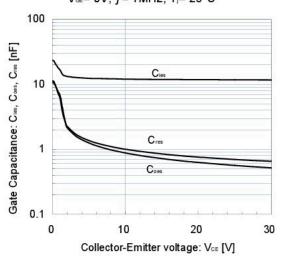
Collector current vs. Collector-Emitter voltage (typ.) T_i = 25°C / chip



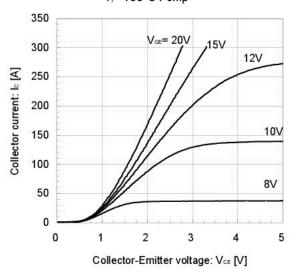
Collector current vs. Collector-Emitter voltage (typ.) $V_{\text{GE}} = 15V \text{ / chip}$



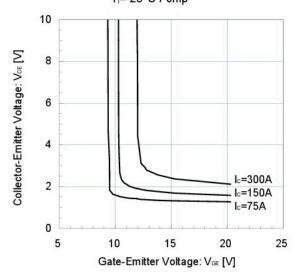
Gate Capacitance vs. Collector-Emitter Voltage V_{GE}= 0V, f=1MHz, T_i= 25°C



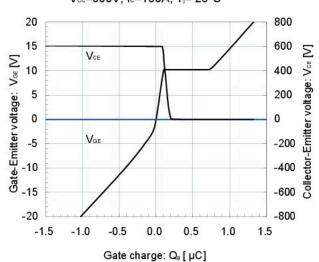
Collector current vs. Collector-Emitter voltage (typ.) T_i= 150°C / chip



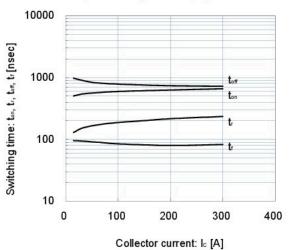
Collector-Emitter voltage vs. Gate-Emitter voltage T_i= 25°C / chip



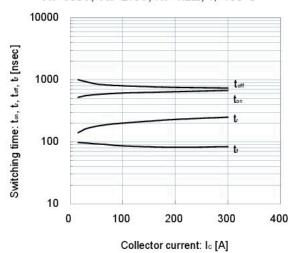
Dynamic Gate Charge (typ.) Voc=600V, Io=150A, T_i= 25°C



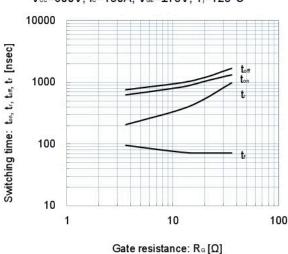
Switching time vs. Collector current (typ.) V_{cc} =600V, V_{ee} =±15V, R_{s} =4.2 Ω , T_{j} =125°C



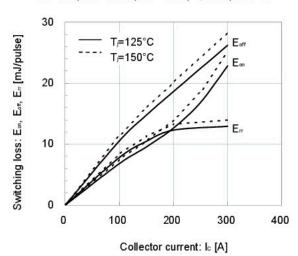
Switching time vs. Collector current (typ.) V_{cc} =600V, V_{ce} =±15V, R_{c} =4.2 Ω , T_{i} =150°C



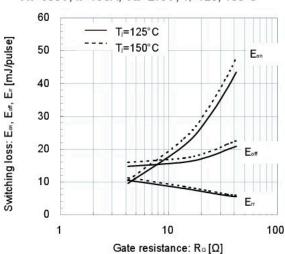
Switching time vs. Gate resistance (typ.) V_{cc} =600V, I_{c} =150A, V_{GE} =±15V, T_{c} =125°C



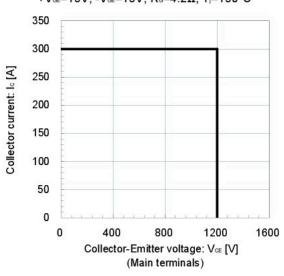
Switching loss vs. Collector current (typ.) V_{cc} =600, V_{ge} =±15V, R_{g} =4.2 Ω , T_{j} =125, 150°C



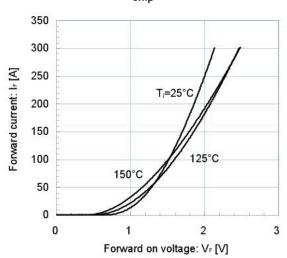
Switching loss vs. Gate resistance (typ.) V_{cc}=600V, I_c=150A, V_{GE}=±15V, T_i=125, 150°C



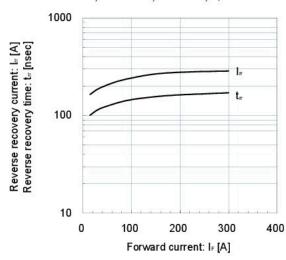
Reverse bias safe operating area (max.) $+V_{GE}=15V$, $-V_{GE}=15V$, $R_{G}=4.2\Omega$, $T_{j}=150^{\circ}C$



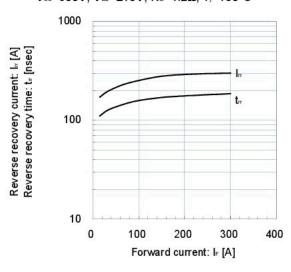
Forward Current vs. Forward Voltage (typ.) chip



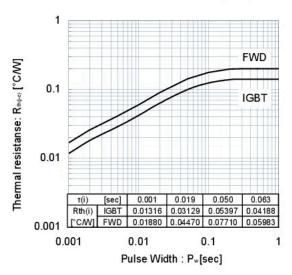
Reverse Recovery Characteristics (typ.) V_{cc} =600V, V_{ce} =±15V, R_c =4.2 Ω , T_j =125°C



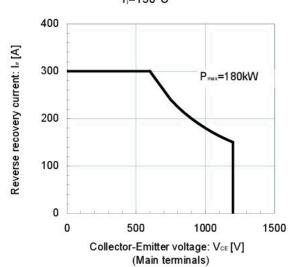
Reverse Recovery Characteristics (typ.) V_{cc} =600V, V_{ge} =±15V, R_{g} =4.2 Ω , T_{j} =150°C



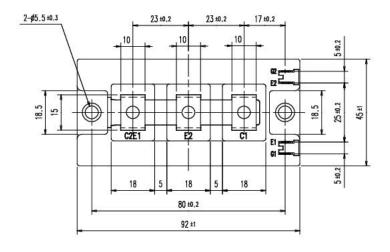
Transient Thermal Resistance (max.)

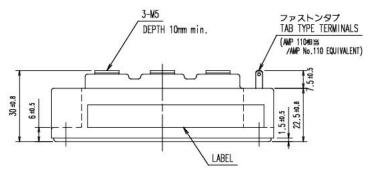


FWD safe operating area (max.) T_i=150°C



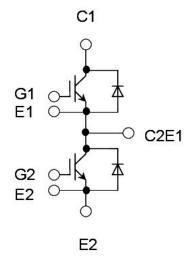
■ Outline Drawings, mm





Weight: 270g (typ.)

■ Equivalent Circuit Schematic



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