



# Winstar Display Co., LTD

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### SPECIFICATION

**CUSTOMER :** \_\_\_\_\_

**MODULE NO.:** **WH2004L-TMI-JT#**

<b>APPROVED BY:</b>		
( FOR CUSTOMER USE ONLY )	<b>PCB VERSION:</b>	<b>DATA:</b>

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

VERSION	DATE	REVISED PAGE NO.	SUMMARY

B	2008/11/6	13	Modify Character Generator ROM Pattern
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**Winstar Display Co., LTD**  
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MODLE NO :

**RECORDS OF REVISION**

DOC. FIRST ISSUE

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2008/4/8		First issue
A	2008.09.30	20	Modify Backlight life time.
B	2008/11/6	13	Modify Character Generator ROM Pattern

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- 2.Precautions in use of LCD Modules
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# 1. Module Classification Information

WH    2004    L-TMI-    JT#  
 ♀    ♂    ♂    ♀ ♂ ♂ ♂    ♂

♀ Brand : WINSTAR DISPLAY CORPORATION

♂ Display Type : H→Character Type, G→Graphic Type

♂ Display Font : Character 20 words, 4Lines.

♂ Model serials no.

♀ Backlight Type :    N→Without backlight                                    T→LED, White  
                                   B→EL, Blue green    A→LED, Amber  
                                   D→EL, Green    R→LED, Red  
                                   W→EL, White    O→LED, Orange  
                                   F→CCFL, White    G→LED, Green  
                                   Y→LED, Yellow Green

♂ LCD Mode :            B→TN Positive, Gray                                    T→FSTN Negative  
                                   N→TN Negative,  
                                   G→STN Positive, Gray  
                                   Y→STN Positive, Yellow Green  
                                   M→STN Negative, Blue  
                                   F→FSTN Positive

♂ LCD Polarize        A→Reflective, N.T, 6:00                                H→Transflective, W.T,6:00  
   Type/ Temperature    D→Reflective, N.T, 12:00                              K→Transflective, W.T,12:00  
   range/ View             G→Reflective, W. T, 6:00                              C→Transmissive, N.T,6:00  
   direction                J→Reflective, W. T, 12:00                             F→Transmissive, N.T,12:00  
                                   B→Transflective, N.T,6:00                            I→Transmissive, W. T, 6:00  
                                   E→Transflective, N.T.12:00                          L→Transmissive, W.T,12:00

♂ Special Code        JT : English and Japanese standard font  
                                   #:Fit in with the ROHS Directions and regulations

## 2 Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8). Winstar have the right to change the passive components
- (9). Winstar have the right to change the PCB Rev.

## 3 .General Specification

Item	Dimension	Unit
Number of Characters	20 characters x 4 Lines	—
Module dimension	146.0 x 62.5 x 13.6(MAX)	mm
View area	123.5 x 43.0	mm
Active area	118.84x 38.47	mm
Dot size	0.92 x 1.1	mm
Dot pitch	0.98 x 1.16	mm
Character size	4.84 x 9.22	mm
Character pitch	6.0 x 9.75	mm
LCD type	STN Negative, Blue Transmissive, (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty	1/16	
View direction	6 o'clock	
Backlight Type	LED White	

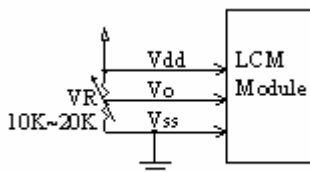
## 4. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	$T_{OP}$	-20	—	+70	°C
Storage Temperature	$T_{ST}$	-30	—	+80	°C
Input Voltage	$V_I$	$V_{SS}$	—	$V_{DD}$	V
Supply Voltage For Logic	$V_{DD}-V_{SS}$	-0.3	—	7	V
Supply Voltage For LCD	$V_{DD}-V_0$	-0.3	—	13	V

## 5. Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	4.5	5.0	5.5	V
Supply Voltage For LCD	$V_{DD}-V_0$	$T_a=-20^{\circ}\text{C}$	—	—	5.4	V
*Note		$T_a=25^{\circ}\text{C}$	—	4.2	—	V
		$T_a=+70^{\circ}\text{C}$	3.5	—	—	V
Input High Volt.	$V_{IH}$	—	$0.7V_{DD}$	—	$V_{DD}$	V
Input Low Volt.	$V_{IL}$	—	$V_{SS}$	—	0.6	V
Output High Volt.	$V_{OH}$	—	3.9	—	—	V
Output Low Volt.	$V_{OL}$	—	—	—	0.4	V
Supply Current	$I_{DD}$	$V_{DD}=5\text{V}$	1.0	1.2	1.5	mA

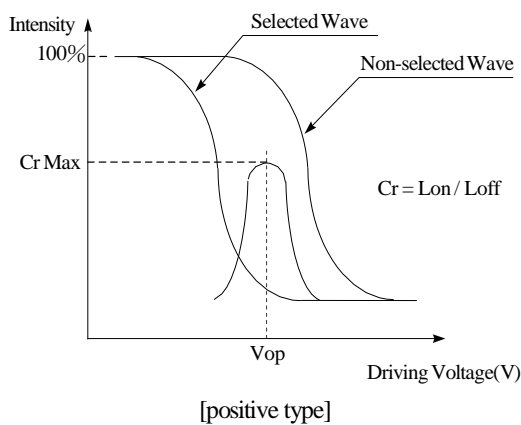
\* Note: Please design the VOP adjustment circuit on customer's main board



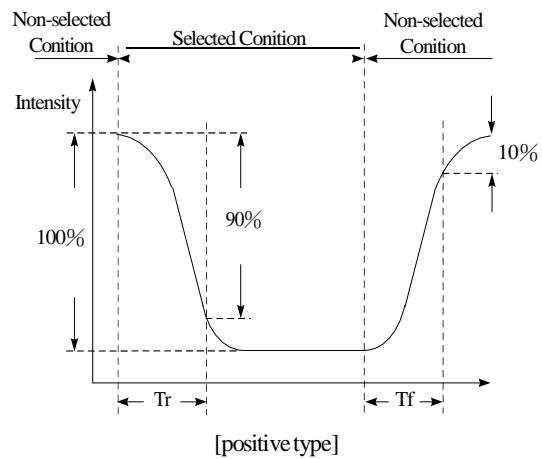
## 6. Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	(V) $\theta$	CR <b>b</b> 2	20	—	40	deg
	(H) $\varphi$	CR <b>b</b> 2	-30	—	30	deg
Contrast Ratio	CR	—	—	3	—	—
Response Time	T rise	—	—	150	200	ms
	T fall	—	—	150	200	ms

### Definition of Operation Voltage (Vop)



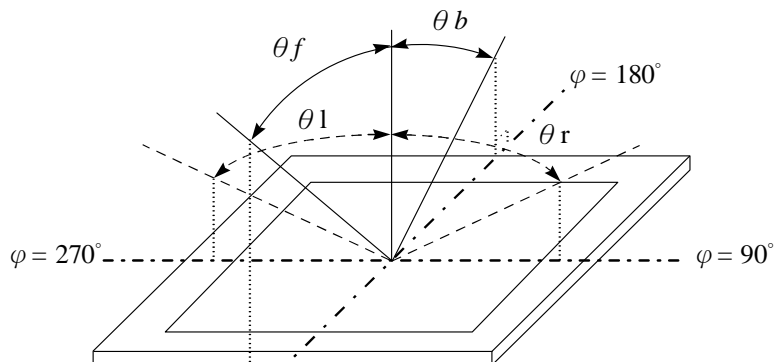
### Definition of Response Time (Tr, Tf)



### Conditions :

Operating Voltage : Vop      Viewing Angle( $\theta$  ,  $\varphi$ ) :  $0^\circ$  ,  $0^\circ$   
 Frame Frequency : 64 HZ      Driving Waveform : 1/N duty , 1/a bias

### Definition of viewing angle(CR**b**2)



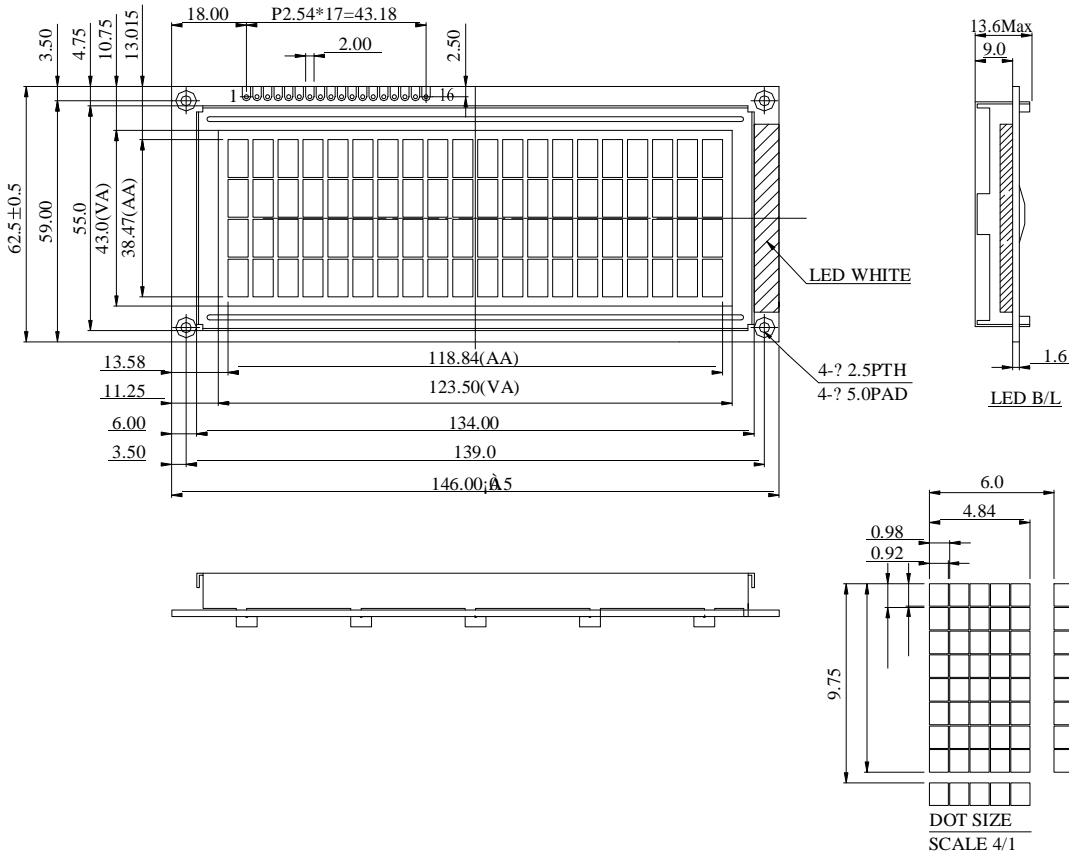


$$\varphi = 0^\circ$$

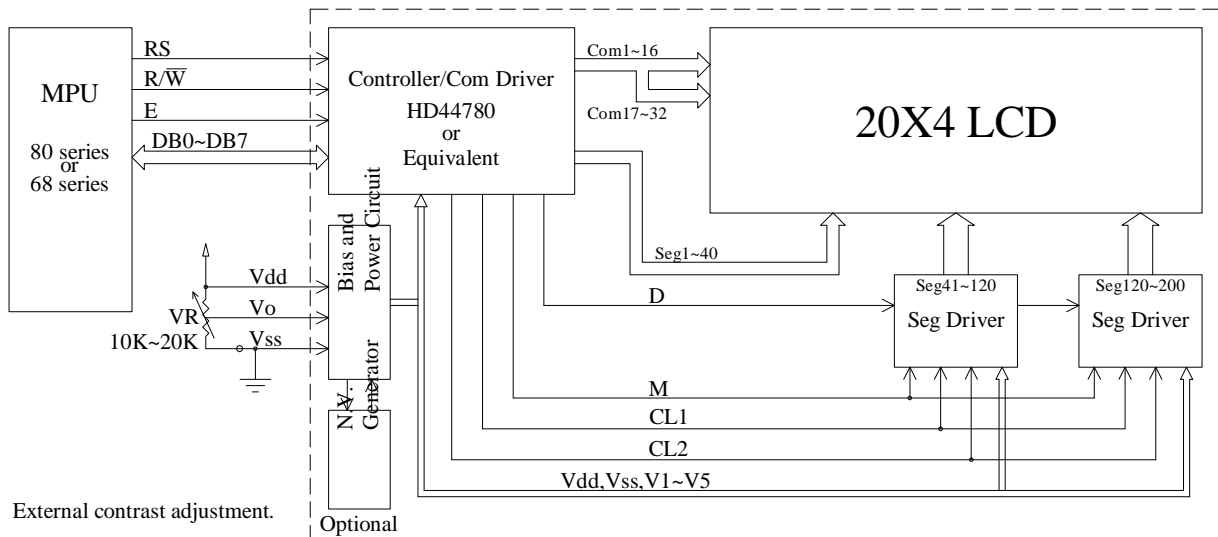
## 7. Interface Pin Function

Pin No.	Symbol	Level	Description
1	V <sub>SS</sub>	0V	Ground
2	V <sub>DD</sub>	5.0V	Supply Voltage for logic
3	VO	(Variable)	Operating voltage for LCD
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(MPU→Module) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
15	A	—	LED +
16	K	—	LED -
17	NC	—	No connection
18	NC	—	No connection

## 8. Contour Drawing & Block Diagram



PIN NO.	SYMBOL
1	Vss
2	Vdd
3	Vo
4	RS
5	R/W
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	A
16	K
17	NC
18	NC



Character located	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	10	11	12	13	
DDRAM address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	58	59	5A	5B
DDRAM address	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27	28
	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67	68

DDRAM address  
DDRAM address

## 9. Function Description

The LCD display Module is built in a LSI controller, the controller has two 8-bit registers, an instruction register (IR) and a data register (DR).

The IR stores instruction codes, such as display clear and cursor shift, and address information for display data RAM (DDRAM) and character generator (CGRAM). The IR can only be written from the MPU. The DR temporarily stores data to be written or read from DDRAM or CGRAM. When address information is written into the IR, then data is stored into the DR from DDRAM or CGRAM. By the register selector (RS) signal, these two registers can be selected.

RS	R/W	Operation
0	0	IR write as an internal operation (display clear, etc.)
0	1	Read busy flag (DB7) and address counter (DB0 to DB7)
1	0	Write data to DDRAM or CGRAM (DR to DDRAM or CGRAM)
1	1	Read data from DDRAM or CGRAM (DDRAM or CGRAM to DR)

### Busy Flag (BF)

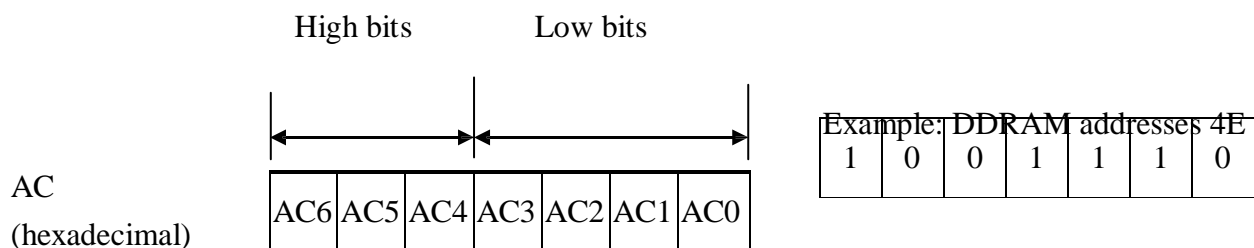
When the busy flag is 1, the controller LSI is in the internal operation mode, and the next instruction will not be accepted. When RS=0 and R/W=1, the busy flag is output to DB7. The next instruction must be written after ensuring that the busy flag is 0.

### Address Counter (AC)

The address counter (AC) assigns addresses to both DDRAM and CGRAM

### Display Data RAM (DDRAM)

This DDRAM is used to store the display data represented in 8-bit character codes. Its extended capacity is 80×8 bits or 80 characters. Below figure is the relationships between DDRAM addresses and positions on the liquid crystal display.



## Display position DDRAM address

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20

00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	10	11	12	13
40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	51	52	53
14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27
54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67

**Character Generator ROM (CGROM)**

The CGROM generate 5×8 dot or 5×10 dot character patterns from 8-bit character codes. See Table 2.

**Character Generator RAM (CGRAM)**

In CGRAM, the user can rewrite character by program. For 5×8 dots, eight character patterns can be written, and for 5×10 dots, four character patterns can be written.

Write into DDRAM the character code at the addresses shown as the left column of table 1. To show the character patterns stored in CGRAM.

Relationship between CGRAM Addresses, Character Codes (DDRAM) and Character patterns

Table 1.

For 5 \* 8 dot character patterns

Character Codes ( DDRAM data )								CGRAM Address					Character Patterns ( CGRAM data )											
7	6	5	4	3	2	1	0	5	4	3	2	1	0	7	6	5	4	3	2	1	0			
High				Low				High			Low		High				Low							
0 0 0 0 * 0 0 0								0 0 0					0 0 0	*	*	*	0							
													0 0 1	*	*	*	0	0	0	0	0	0	0	0
													0 1 0	*	*	*	0	0	0	0	0	0	0	0
													0 1 1	*	*	*	0	0	0	0	0	0	0	0
													1 0 0	*	*	*	0	0	0	0	0	0	0	0
													1 0 1	*	*	*	0	0	0	0	0	0	0	0
													1 1 0	*	*	*	0	0	0	0	0	0	0	0
													1 1 1	*	*	*	0	0	0	0	0	0	0	0
													0 0 0	*	*	*	0	0	0	0	0	0	0	0
													0 0 1	*	*	*	0	0	0	0	0	0	0	0
0 0 0 0 * 0 0 1								0 0 1					0 1 0	*	*	*	0	0	0	0	0	0	0	
													0 1 1	*	*	*	0	0	0	0	0	0	0	
													1 0 0	*	*	*	0	0	0	0	0	0	0	
													1 0 1	*	*	*	0	0	0	0	0	0	0	
1 1 0	*	*	*	0	0	0	0	0	0	0														
1 1 1	*	*	*	0	0	0	0	0	0	0														
0 0 0 0 * 1 1 1								1 1 1					1 0 0	*	*	*								
													1 0 1	*	*	*								
													1 1 0	*	*	*								
													1 1 1	*	*	*								

Character pattern ( 1 )

Cursor pattern

Character pattern ( 2 )

Cursor pattern

For 5 \* 10 dot character patterns

Character Codes ( DDRAM data )								CGRAM Address					Character Patterns ( CGRAM data )																										
7	6	5	4	3	2	1	0	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0										
High				Low				High			Low		High					Low																					
0 0 0 0 * 0 0 0								0 0					0 0 0 0	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
													0 0 0 1	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													0 0 1 0	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													0 0 1 1	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													0 1 0 0	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													0 1 0 1	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													0 1 1 0	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													0 1 1 1	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													1 0 0 0	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
													1 0 0 1	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1 0 1 0	*	*	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
														*	*	*																							
														*	*	*																							
								1 1 1 1						*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*							
														*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*

Character pattern

Cursor pattern

■ : " High "

# 10 .Character Generator ROM Pattern

Table.2

Upper 4 bit Lower 4 bit	LLLL	LLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)			0	1	2	3	4				5	6	7	8	9
LLLH	(2)		!	0	1	2	3	4			5	6	7	8	9	0
LLHL	(3)		"	2	3	4	5	6			7	8	9	0	1	2
LLHH	(4)		#	3	4	5	6	7			8	9	0	1	2	3
LHLL	(5)		\$	4	5	6	7	8			9	0	1	2	3	4
LHLH	(6)		%	5	6	7	8	9			0	1	2	3	4	5
LHHL	(7)		&	6	7	8	9	0			1	2	3	4	5	6
LHHH	(8)		'	7	8	9	0	1			2	3	4	5	6	7
HLLL	(1)		(	8	9	0	1	2			3	4	5	6	7	8
HLLH	(2)		)	9	0	1	2	3			4	5	6	7	8	9
HLHL	(3)		*	0	1	2	3	4			5	6	7	8	9	0
HLHH	(4)		+	1	2	3	4	5			6	7	8	9	0	1
HHLL	(5)		,	2	3	4	5	6			7	8	9	0	1	2
HHLH	(6)		-	3	4	5	6	7			8	9	0	1	2	3
HHHL	(7)		.	4	5	6	7	8			9	0	1	2	3	4
HHHH	(8)		/	5	6	7	8	9			0	1	2	3	4	5



## 11. Instruction Table

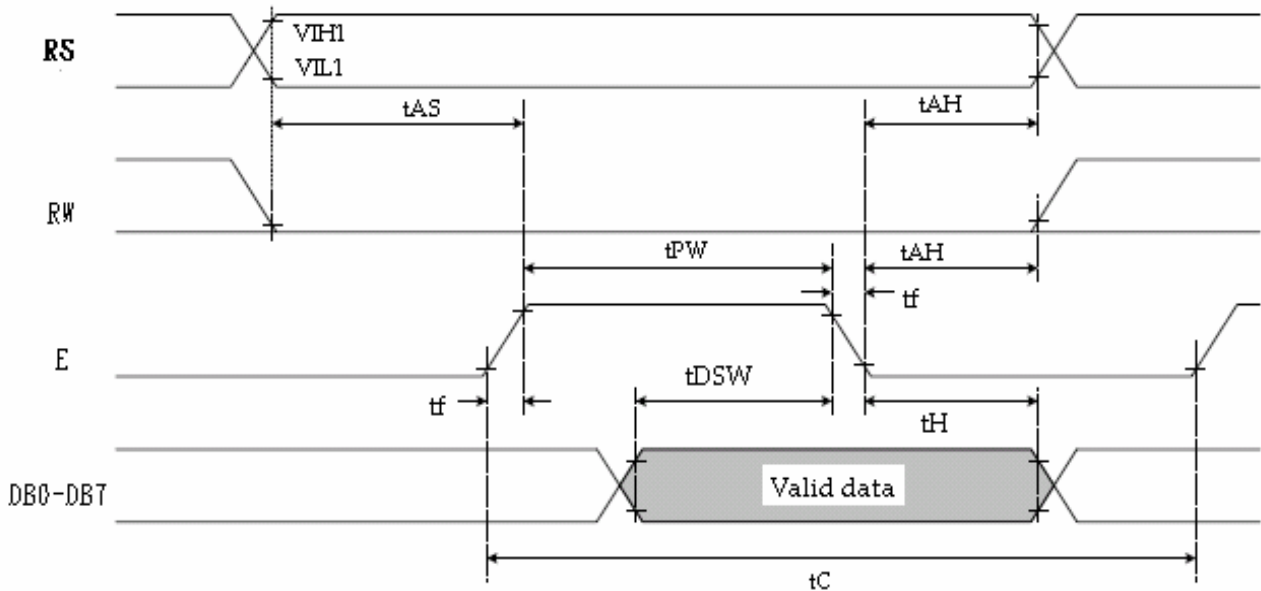
Instruction	Instruction Code										Description	Execution time (fosc=270Khz)	
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0			
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "00H" to DDRAM and set DDRAM address to "00H" from AC	1.53ms	
Return Home	0	0	0	0	0	0	0	0	0	1	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53ms	
Entry Mode Set	0	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39 $\mu$ s
Display ON/OFF Control	0	0	0	0	0	0	0	1	D	C	B	Set display (D), cursor (C), and blinking of cursor (B) on/off control bit.	39 $\mu$ s
Cursor or Display Shift	0	0	0	0	0	0	1	S/C	R/L	—	—	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39 $\mu$ s
Function Set	0	0	0	0	0	1	DL	N	F	—	—	Set interface data length (DL:8-bit/4-bit), numbers of display line (N:2-line/1-line)and, display font type (F:5x11 dots/5x8 dots)	39 $\mu$ s
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39 $\mu$ s	
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39 $\mu$ s	
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 $\mu$ s	
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43 $\mu$ s	
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43 $\mu$ s	

◆ "—" : don't care

## 12 .Timing Characteristics

### 12.1 Write Operation

- Writing data from MPU

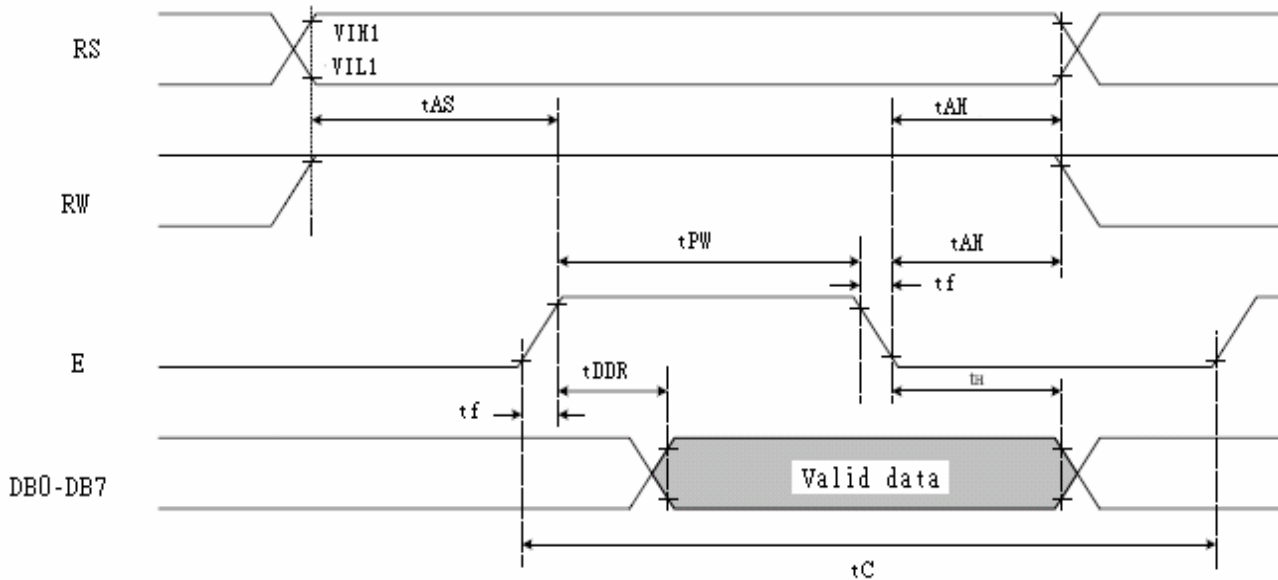


$T_a=25^{\circ}\text{C}$ ,  $V_{DD}=5\text{V}$

Item	Symbol	Min	Typ	Max	Unit
Enable cycle time	$T_C$	1200	—	—	ns
Enable pulse width	$T_{PW}$	140	—	—	ns
Enable rise/fall time	$T_R, T_F$	—	—	25	ns
Address set-up time (RS, R/W to E)	$t_{AS}$	0	—	—	ns
Address hold time	$t_{AH}$	10	—	—	ns
Data set-up time	$t_{DSW}$	40	—	—	ns
Data hold time	$t_H$	10	—	—	ns

## 12.2 Read Operation

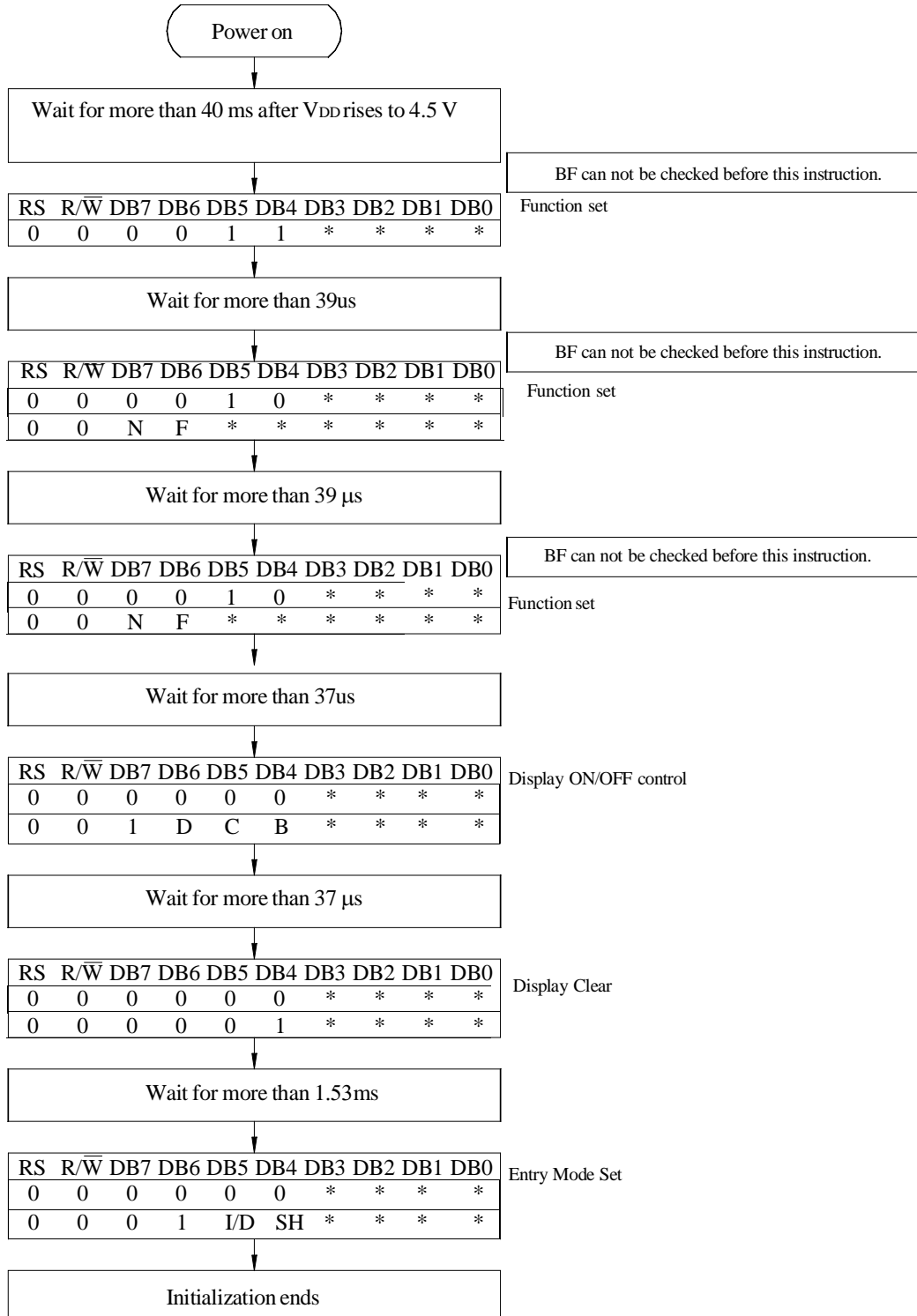
- Reading data from ST7066U



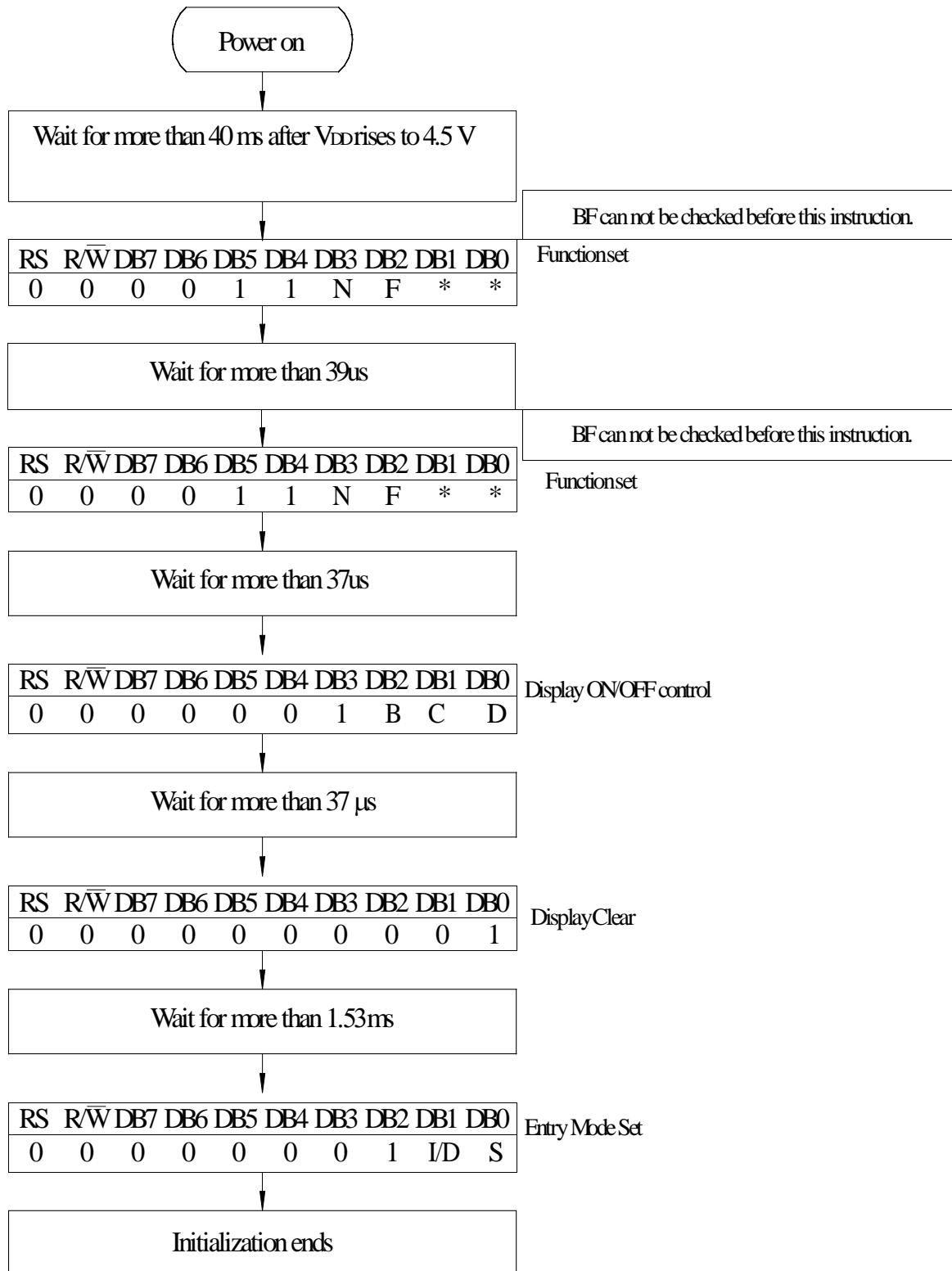
$T_a=25^{\circ}\text{C}$ ,  $V_{DD}=5\text{V}$

Item	Symbol	Min	Typ	Max	Unit
Enable cycle time	$T_C$	1200	—	—	ns
Enable pulse width (high level)	$T_{PW}$	140	—	—	ns
Enable rise/fall time	$T_R, T_F$	—	—	25	ns
Address set-up time (RS, R/W to E)	$t_{AS}$	0	—	—	ns
Address hold time	$t_{AH}$	10	—	—	ns
Data delay time	$t_{DDR}$	—	—	100	ns
Data hold time	$t_H$	10	—	—	ns

## 13 .Initializing of LCM



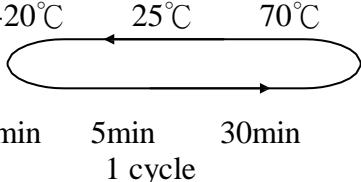
4-Bit Ineterface



8-Bit Ineterface

## 14 Reliability

### Content of Reliability Test ( Wide temperature,-2 0°C~70°C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation  <p style="text-align: center;">-20°C      25°C      70°C</p> <p style="text-align: center;">30min    5min    30min</p> <p style="text-align: center;">1 cycle</p>	-20°C / 70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	—

**Note1: No dew condensation to be observed.**

**Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.**

**Note3: Vibration test will be conducted to the product itself without putting it in a container.**



## 15 Backlight Information

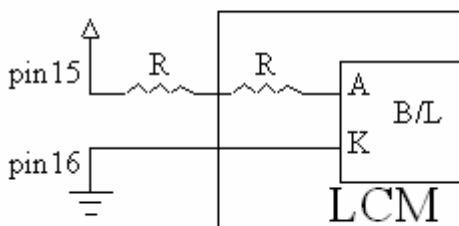
### Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	I <sub>LED</sub>	72	80	125	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	—
Reverse Voltage	V <sub>R</sub>	—	—	5	V	—
Luminous Intensity	I <sub>V</sub>	232	290	—	CD/M <sup>2</sup>	I <sub>LED</sub> =80mA
Wave Length	λ <sub>p</sub>	—	—	—	nm	I <sub>LED</sub> =80mA
LED Life Time (For Reference only)	—	—	50K	—	Hr.	I <sub>LED</sub> =80mA 25°C,50-60%RH, (Note 1)
Color	White					

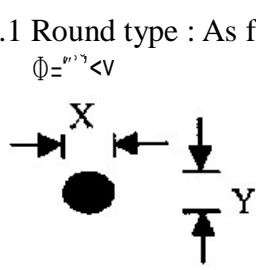
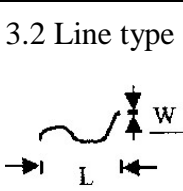
**Note:** The LED of B/L is drive by current only, drive voltage is for reference only.  
drive voltage can make driving current under safety area (current between minimum and maximum).

**Note 1:**50K hours is only an estimate for reference.

2. Drive from pin15, pin16



## 16. Inspection specification

NO	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65												
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leq 0.25\text{mm}$ , no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5												
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing  <table border="1" data-bbox="869 1048 1353 1294"> <thead> <tr> <th><math>\Phi</math></th> <th>Acceptable QTY</th> </tr> </thead> <tbody> <tr> <td><math>\Phi &gt; 0.25</math></td> <td>0</td> </tr> <tr> <td><math>0.1 &lt; \Phi \leq 0.25</math></td> <td>1</td> </tr> <tr> <td><math>0.05 &lt; \Phi \leq 0.1</math></td> <td>2</td> </tr> <tr> <td><math>\Phi \leq 0.05</math></td> <td>&gt;</td> </tr> </tbody> </table>	$\Phi$	Acceptable QTY	$\Phi > 0.25$	0	$0.1 < \Phi \leq 0.25$	1	$0.05 < \Phi \leq 0.1$	2	$\Phi \leq 0.05$	>	2.5		
		$\Phi$	Acceptable QTY												
$\Phi > 0.25$	0														
$0.1 < \Phi \leq 0.25$	1														
$0.05 < \Phi \leq 0.1$	2														
$\Phi \leq 0.05$	>														
3.2 Line type : (As following drawing)  <table border="1" data-bbox="710 1400 1353 1646"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable QTY</th> </tr> </thead> <tbody> <tr> <td>---</td> <td><math>W &gt; 0.25</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>L &gt; 0.25</math></td> <td><math>0.02 &lt; W &gt; 0.25</math></td> <td rowspan="2">2</td> </tr> <tr> <td><math>L &gt; 0.25</math></td> <td><math>0.03 &lt; W &gt; 0.25</math></td> </tr> <tr> <td>---</td> <td><math>0.05 &lt; W</math></td> <td>As round type</td> </tr> </tbody> </table>	Length	Width	Acceptable QTY	---	$W > 0.25$	Accept no dense	$L > 0.25$	$0.02 < W > 0.25$	2	$L > 0.25$	$0.03 < W > 0.25$	---	$0.05 < W$	As round type	2.5
Length	Width	Acceptable QTY													
---	$W > 0.25$	Accept no dense													
$L > 0.25$	$0.02 < W > 0.25$	2													
$L > 0.25$	$0.03 < W > 0.25$														
---	$0.05 < W$	As round type													

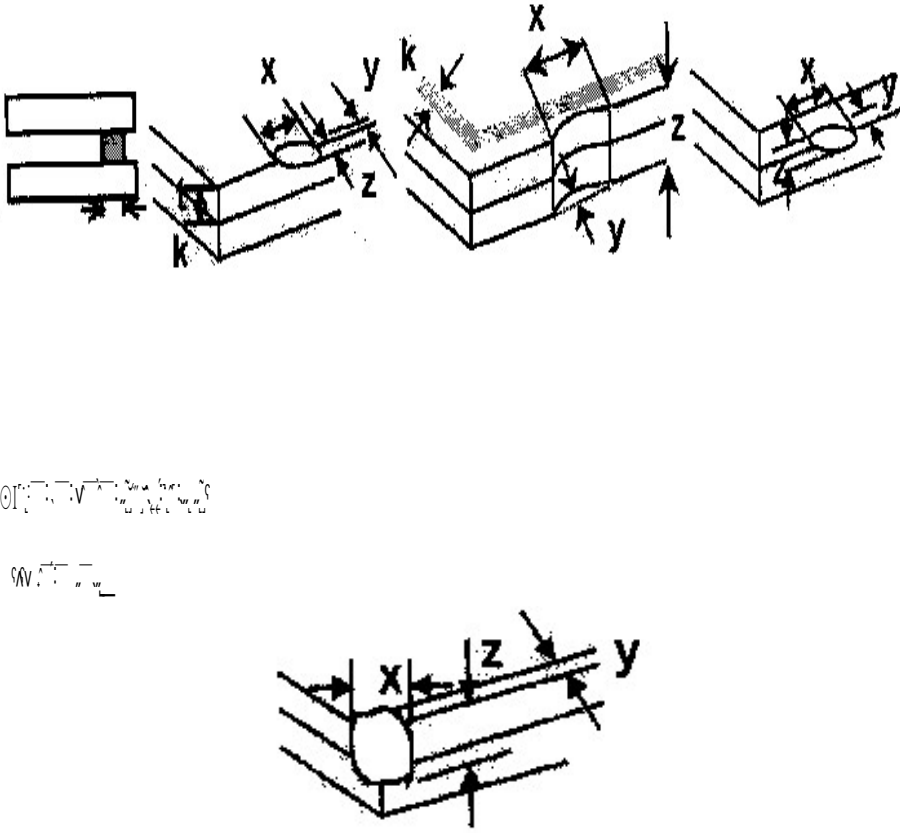
Size $\Phi$	Acceptable QTY
$\Phi > 0.25$	Accept no dense
$0.1 < \Phi \leq 0.25$	3
$0.05 < \Phi \leq 0.1$	2
$\Phi \leq 0.05$	0
Total QTY	3

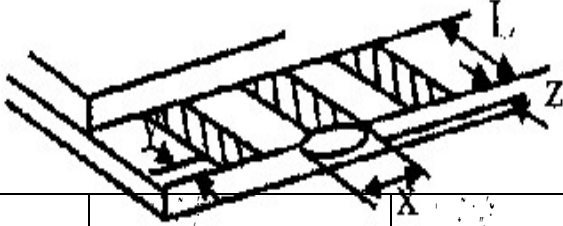
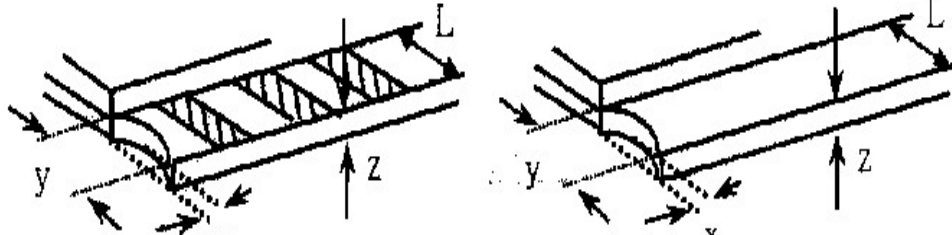
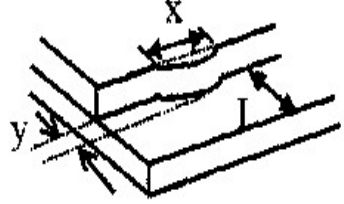
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.	2.5
----	----------------------	---	-----

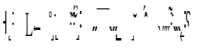
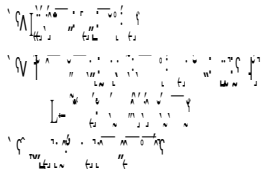
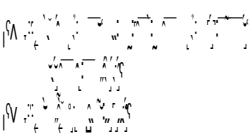
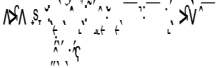
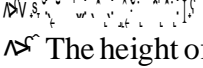

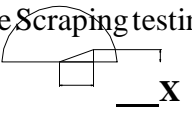

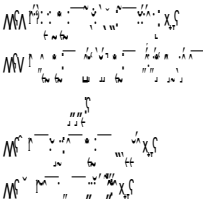
NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination	

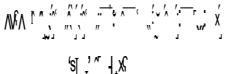
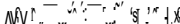
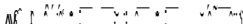
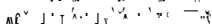
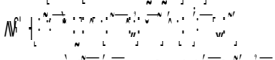
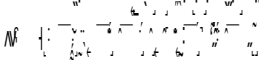

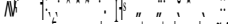
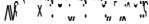
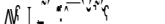
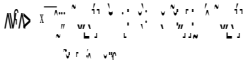
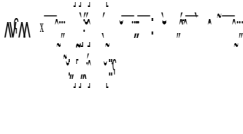
"jbk"		"jbk"
"jbk"		"jbk"

"jbk"		"jbk"
"jbk"		"jbk"

<p>06</p>	<p>Chipped glass</p>	<p>Symbols Define:  x: Chip length            y: Chip width            z: Chip thickness  k: Seal width            t: Glass thickness       a: LCD side length  L: Electrode pad length:</p> <p>6.1 General glass chip :  6.1.1 Chip on panel surface and crack between panels:</p> 	<p>2.5</p>
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NO	Item	Criterion	AQL																
06	Glass crack	<p>x: Chip length      y: Chip width      z: Chip thickness                      k: Seal width      t: Glass thickness      a: LCD side length                      L: Electrode pad length</p> <p>6.2 Protrusion over terminal :                      6.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="354 734 1264 891"> <tr> <td><math>\frac{x}{L}</math></td> <td><math>\frac{y}{k}</math></td> <td><math>&gt; &lt; \frac{z}{t}</math></td> </tr> <tr> <td><math>\frac{x}{L}</math></td> <td><math>\frac{y}{k}</math></td> <td><math>&gt; &lt; \frac{z}{t}</math></td> </tr> </table> <p>W</p>  <table border="1" data-bbox="427 1227 1264 1384"> <tr> <td><math>\frac{x}{L}</math></td> <td><math>\frac{y}{k}</math></td> <td><math>&gt; &lt; \frac{z}{t}</math></td> </tr> <tr> <td><math>\frac{x}{L}</math></td> <td><math>\frac{y}{k}</math></td> <td><math>&gt; &lt; \frac{z}{t}</math></td> </tr> </table> <p>W</p>  <table border="1" data-bbox="762 1608 1273 1765"> <tr> <td><math>\frac{x}{L}</math></td> <td><math>\frac{y}{k}</math></td> </tr> <tr> <td><math>\frac{x}{L}</math></td> <td><math>\frac{y}{k}</math></td> </tr> </table>	$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$	$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$	$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$	$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$	$\frac{x}{L}$	$\frac{y}{k}$	$\frac{x}{L}$	$\frac{y}{k}$	2.5
$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$																	
$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$																	
$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$																	
$\frac{x}{L}$	$\frac{y}{k}$	$> < \frac{z}{t}$																	
$\frac{x}{L}$	$\frac{y}{k}$																		
$\frac{x}{L}$	$\frac{y}{k}$																		

NO	Item	Criterion	AQL
07	Cracked glass		2.5
08	Backlight elements		0.65 2.5 0.65
09	Bezel		2.5 0.65
10	PCB、COB	  <p>  The height of the COB should not exceed the height indicated in the assembly diagram.         </p> <p>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.</p> <p>10.5 No oxidation or contamination PCB terminals.</p> <p>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.</p> <p>10.7 The jumper on the PCB should conform to the product characteristic chart.</p> <p>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.</p> <p>       <math>X * Y \leq 2\text{mm}^2</math> </p>	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering		2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance		2.5
			0.65
			2.5
			2.5
			2.5
			2.5
			2.5
			0.65
			0.65
			0.65
			0.65
			0.65



## 17. Material List of Components for RoHs

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark “#”in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

### Exhibit A : The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

### 2.Process for RoHS requirement :

(1) Use the Sn/Ag/Cu soldering surface ; the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp. :

Reflow : 250°C,30 seconds Max. ;

Connector soldering wave or hand soldering : 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5°C ;

Recommended customer's soldering temp. of connector : 280°C, 3 seconds.



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**LCM Sample Estimate Feedback Sheet**

Module Number : \_\_\_\_\_

Page: 1

**1、Panel Specification :**

1. Panel Type :  Pass  NG , \_\_\_\_\_
2. View Direction :  Pass  NG , \_\_\_\_\_
3. Numbers of Dots :  Pass  NG , \_\_\_\_\_
4. View Area :  Pass  NG , \_\_\_\_\_
5. Active Area :  Pass  NG , \_\_\_\_\_
6. Operating Temperature :  Pass  NG , \_\_\_\_\_
7. Storage Temperature :  Pass  NG , \_\_\_\_\_
8. Others : \_\_\_\_\_

**2、Mechanical Specification :**

1. PCB Size :  Pass  NG , \_\_\_\_\_
2. Frame Size :  Pass  NG , \_\_\_\_\_
3. Material of Frame :  Pass  NG , \_\_\_\_\_
4. Connector Position :  Pass  NG , \_\_\_\_\_
5. Fix Hole Position :  Pass  NG , \_\_\_\_\_
6. Backlight Position :  Pass  NG , \_\_\_\_\_
7. Thickness of PCB :  Pass  NG , \_\_\_\_\_
8. Height of Frame to PCB :  Pass  NG , \_\_\_\_\_
9. Height of Module :  Pass  NG , \_\_\_\_\_
10. Others :  Pass  NG , \_\_\_\_\_

**3、Relative Hole Size :**

1. Pitch of Connector :  Pass  NG , \_\_\_\_\_
2. Hole size of Connector :  Pass  NG , \_\_\_\_\_
3. Mounting Hole size :  Pass  NG , \_\_\_\_\_
4. Mounting Hole Type :  Pass  NG , \_\_\_\_\_
5. Others :  Pass  NG , \_\_\_\_\_

**4、Backlight Specification :**

1. B/L Type :  Pass  NG , \_\_\_\_\_
2. B/L Color :  Pass  NG , \_\_\_\_\_
3. B/L Driving Voltage (Reference for LED Type) :  Pass  NG , \_\_\_\_\_
4. B/L Driving Current :  Pass  NG , \_\_\_\_\_

5. Brightness of B/L :  Pass  NG , \_\_\_\_\_

6. B/L Solder Method :  Pass  NG , \_\_\_\_\_

7. Others :  Pass  NG , \_\_\_\_\_

>> **Go to page 2** <<



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Module Number : \_\_\_\_\_

Page: 2

**5、Electronic Characteristics of Module :**

- |                              |                               |                                     |
|------------------------------|-------------------------------|-------------------------------------|
| 1. Input Voltage :           | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Supply Current :          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Driving Voltage for LCD : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Contrast for LCD :        | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. B/L Driving Method :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Negative Voltage Output : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Interface Function :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. LCD Uniformity :          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. ESD test :                | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others :                 | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**6、Summary :**

Sales signature : \_\_\_\_\_

Customer Signature : \_\_\_\_\_

Date :    /    /    \_\_\_\_\_